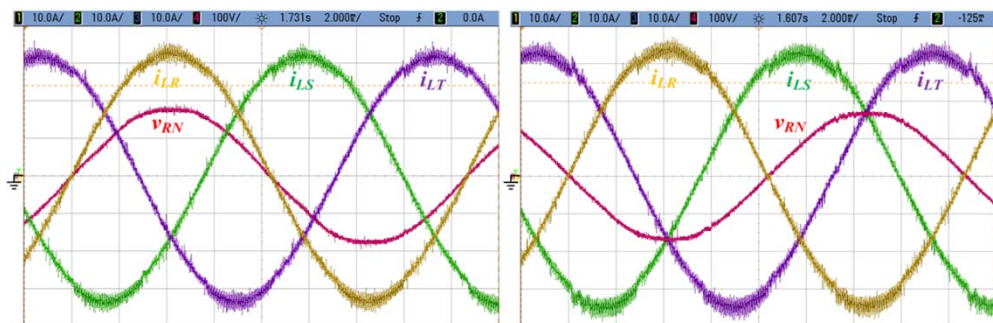


## ▶ 三相四線穩態測試

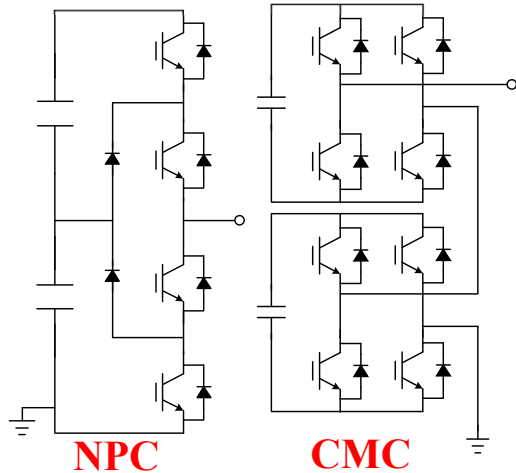


市電併聯模式

整流模式

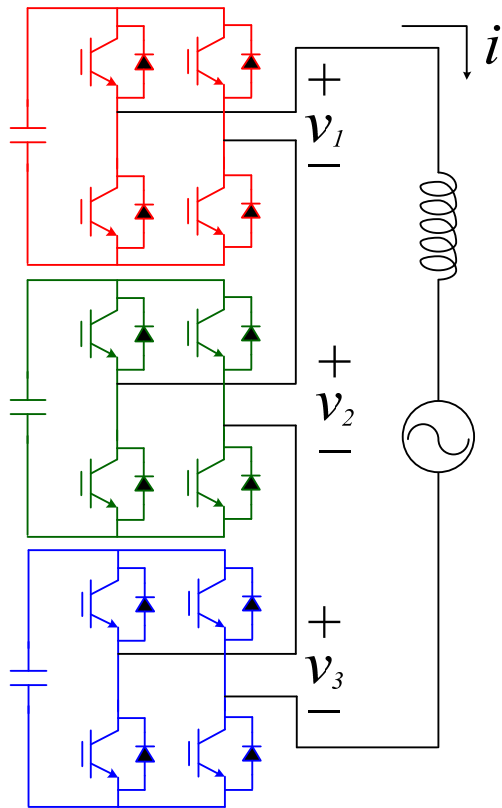
Case	Harmonic order	%	Measured Item	Without FCCC (GC Mode)	With FCCC (GC Mode)	Without FCCC (Rectification Mode)	With FCCC (Rectification Mode)
Case I	5	9.8	PF	0.96	0.98	0.96	0.98
	7	15.8	V <sub>THD</sub> (%)	18.5	18.4	18.4	18.4
	8	2.16	I <sub>THD</sub> (%)	18.8	3.2	18.7	2.8
Case II	3	4.9	PF	0.98	0.99	0.98	0.99
	5	1.6	V <sub>THD</sub> (%)	6.4	6.4	6.4	6.4
	7	2.7					
	11	1.4					
	15	2	I <sub>THD</sub> (%)	9.4	3.8	9.3	3.3
17	1.1						
Case III	3	17.8	PF	0.96	0.98	0.96	0.98
			V <sub>THD</sub> (%)	17.8	17.7	17.7	17.8
			I <sub>THD</sub> (%)	17.7	2.1	17.8	2.0
Case IV	7	4.6	PF	0.97	0.98	0.97	0.98
	9	1	V <sub>THD</sub> (%)	4.9	5.1	4.9	5.0
	21	0.9					
	39	0.7	I <sub>THD</sub> (%)	5.1	2.5	5.1	2.6

### (三) 虛功補償器研製



- **Neutral point clamped converter (NPC)**
  - Unbalanced blocking voltage of diodes
  - Implementation becomes complicated as voltage level increases
- **Cascaded multilevel converter (CMC)**
  - Modular structure is suitable for high output voltage levels.

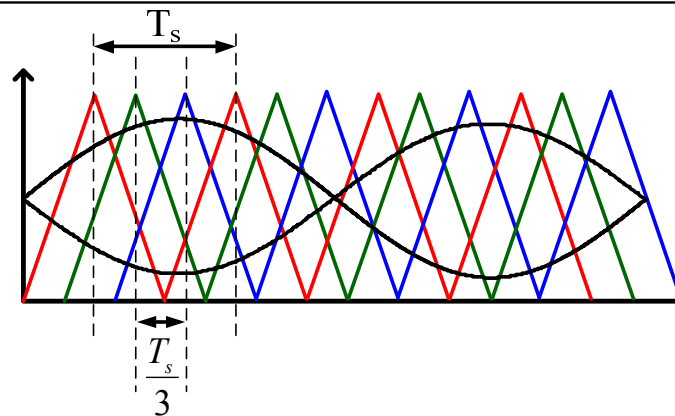
	NPC converter	CMC converter
Voltage level	20	23
DC bus voltage	16.5 kV	850 V
Switches number	3*40	3*44
Capacitors	3*19	3*11

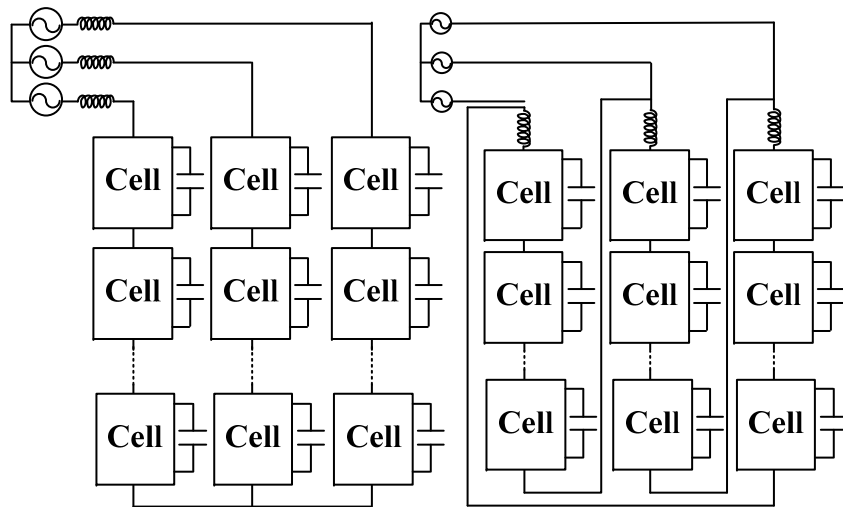
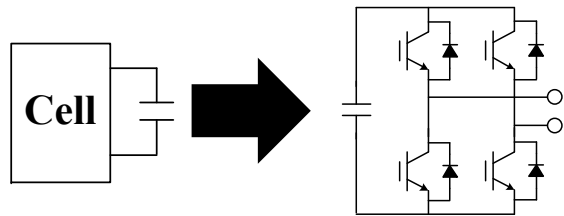


## ■ Phase shift PWM (PSPWM)

- Low switching frequency
- Nice control performance

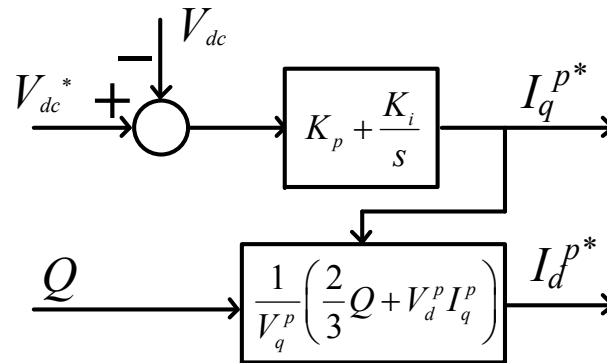
Switching frequency	$1/T_s$
Sampling frequency	$N/T_s$
Equivalent switching frequency of system	$N/T_s$

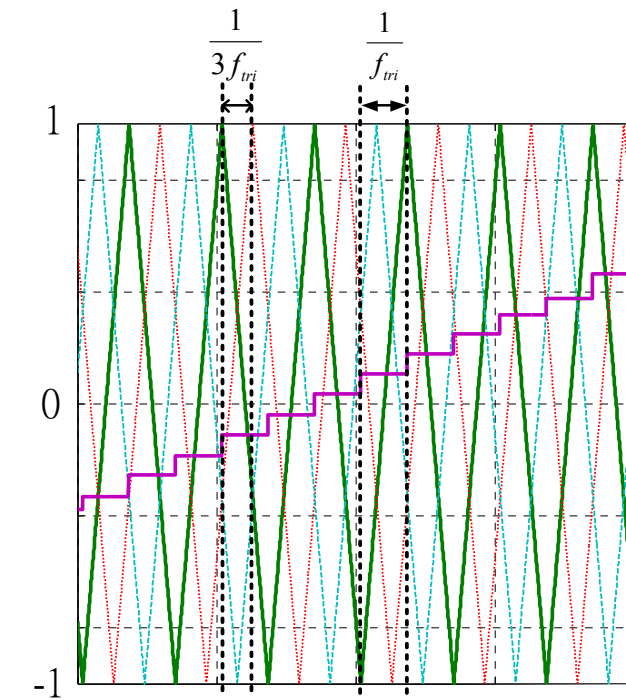
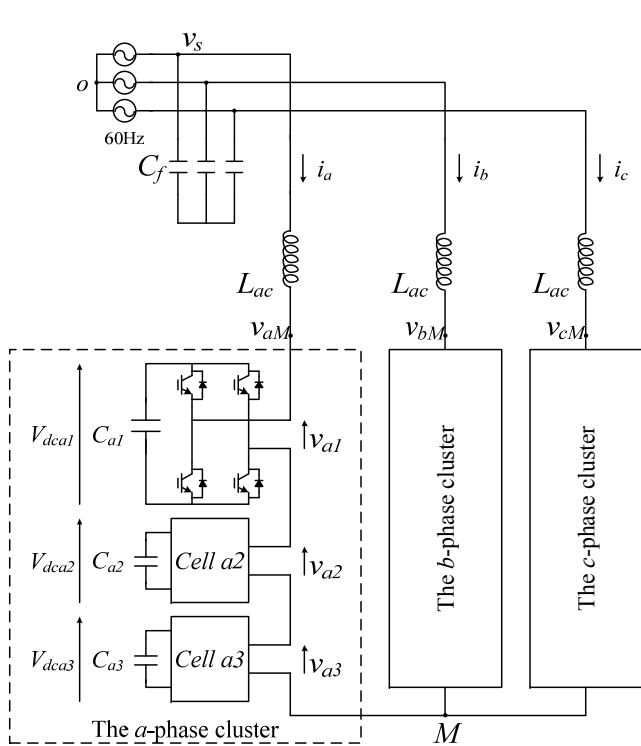




- **Single-Star Bridge Cell**
  - Positive sequence current compensation (STATCOMs)
  - No circulating current
  - Zero sequence voltage injection
- **Single-Delta Bridge Cell**
  - Negative sequence current compensation (flicker comp.)
  - One degree of freedom of circulating current

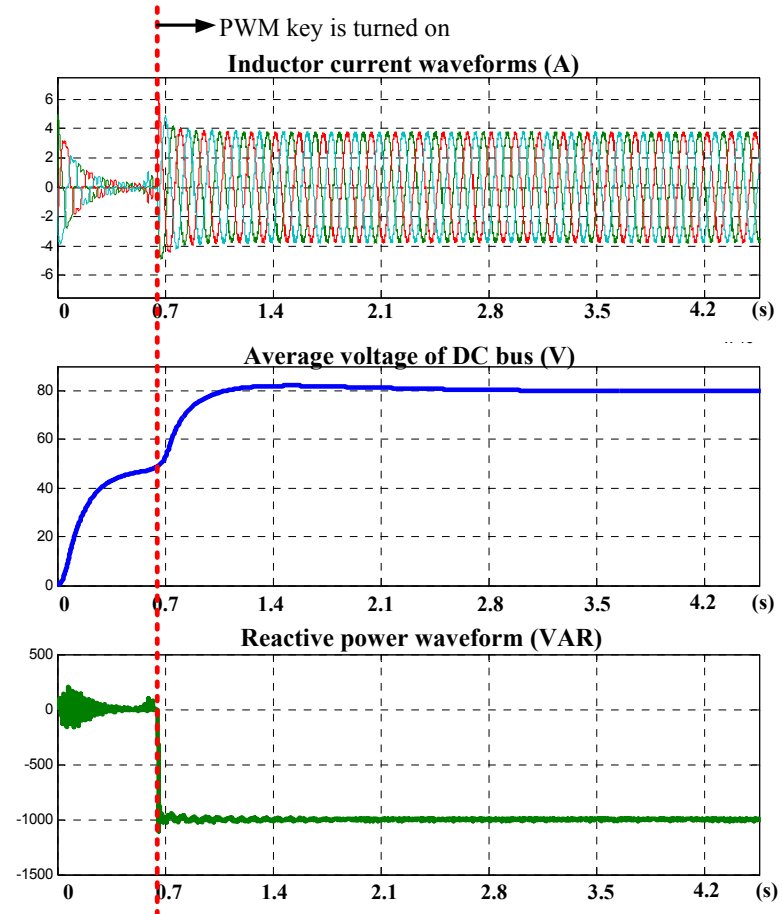
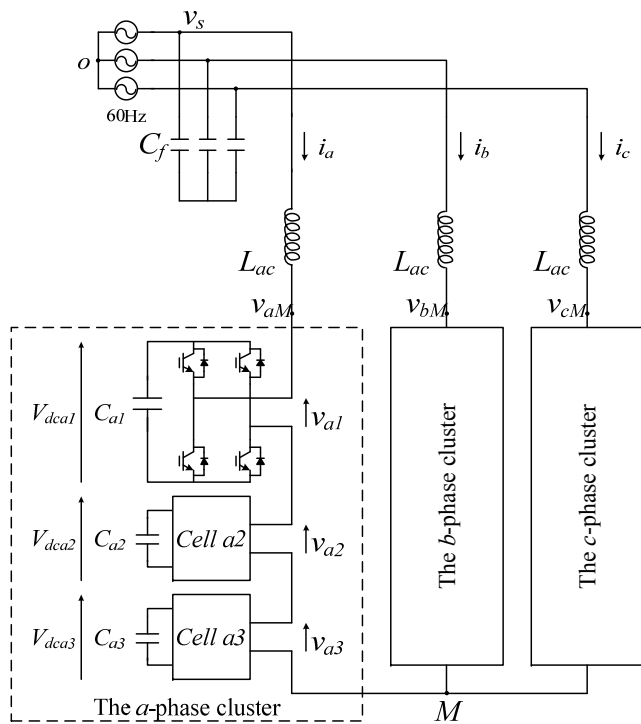
$$\begin{cases} P = \frac{3}{2}(v_\alpha i_\alpha + v_\beta i_\beta) \\ Q = \frac{3}{2}(v_\alpha i_\beta - v_\beta i_\alpha) \end{cases} \Rightarrow \begin{cases} P = \frac{3}{2}(V_q^p I_q^p + V_d^p I_d^p) \\ Q = \frac{3}{2}(V_q^p I_d^p - V_d^p I_q^p) \end{cases} \Rightarrow \begin{aligned} &I_q^{p*} \text{ is for overall voltage control} \\ &I_d^{p*} = \frac{1}{V_q^p} \left( \frac{2}{3} Q + V_d^p I_q^p \right) \end{aligned}$$



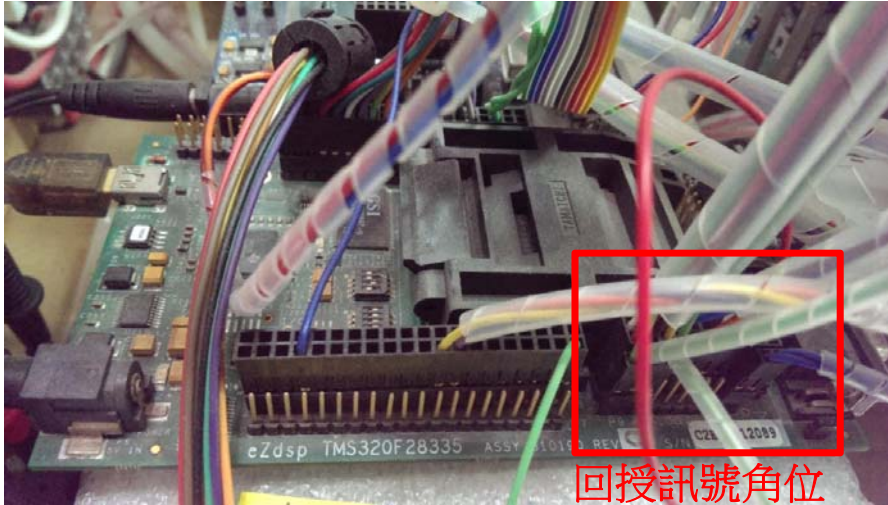


**Modulation index and carrier waveform**

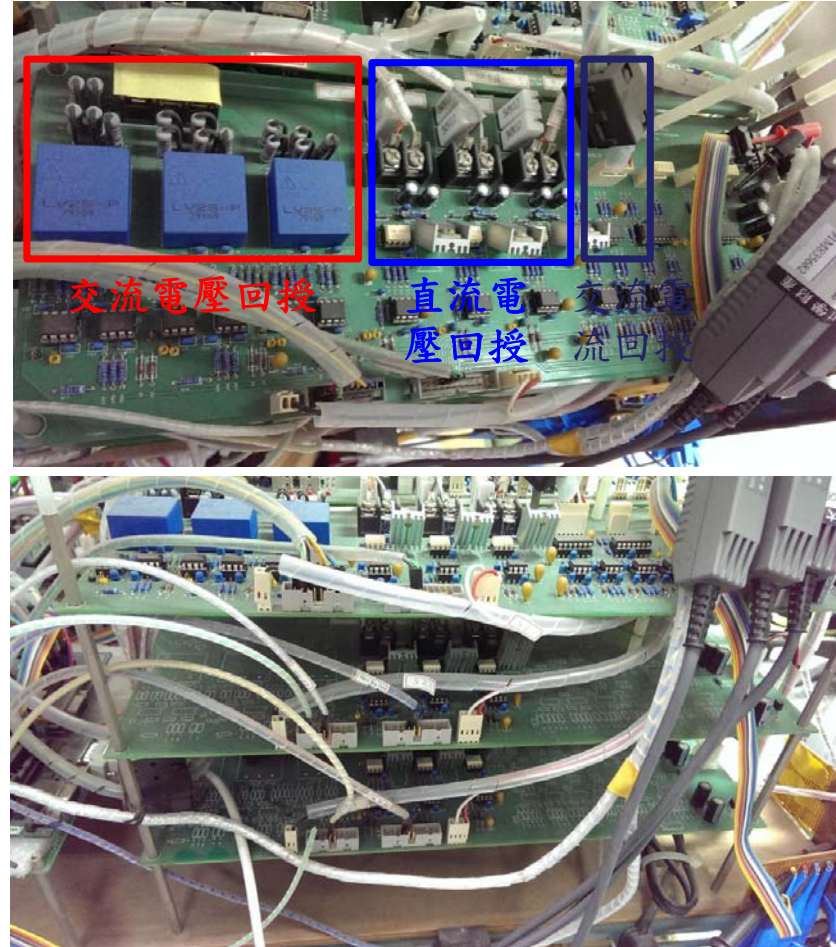
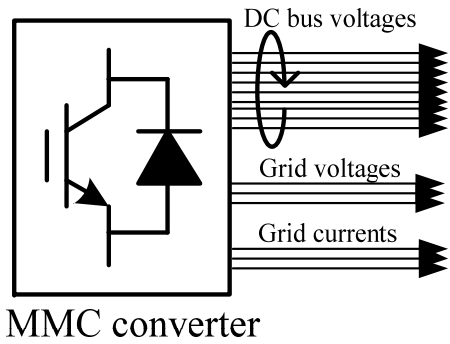
PWM carrier frequency	$f_{tri}$	2 kHz
Equivalent PWM switching frequency	$f_{sw}$	12 kHz
Dead time	$t_{dead}$	1 $\mu$ sec
Reactive power command	$Q^*$	1 kVA
DC bus voltage command	$V_{dc}^*$	80 V







DSP 控制模組





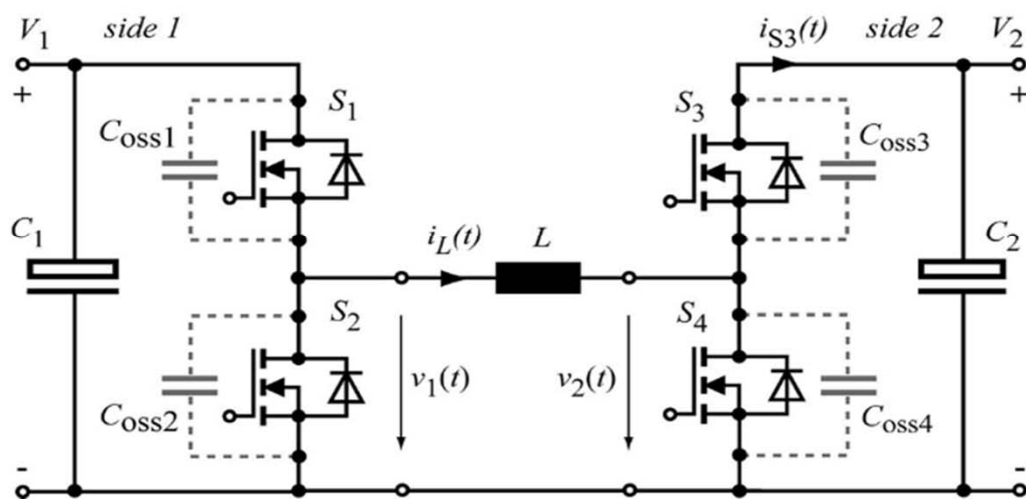
## (四)充放電器研製

目標：

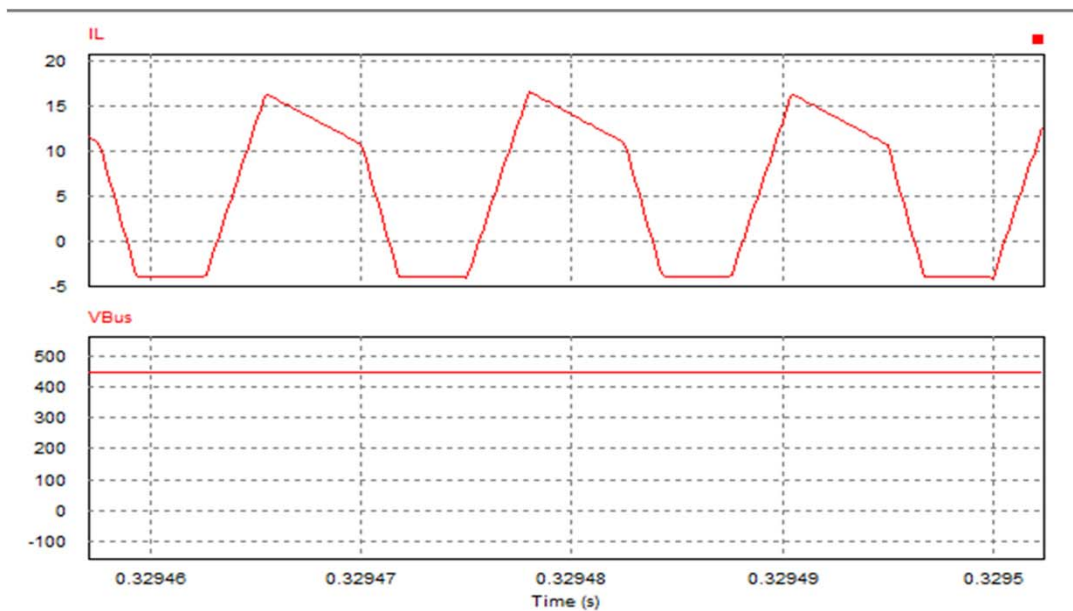
電池側 :380 V

電網側 :420 V

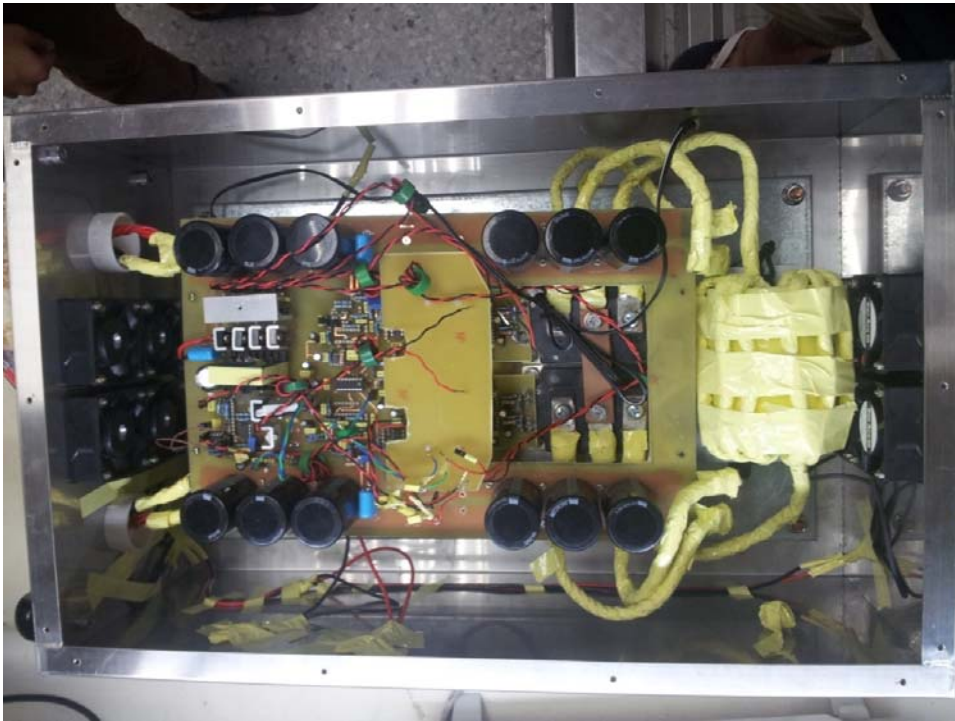
輸出功率：(單組5 kW，總瓦數並聯4組20 kW)



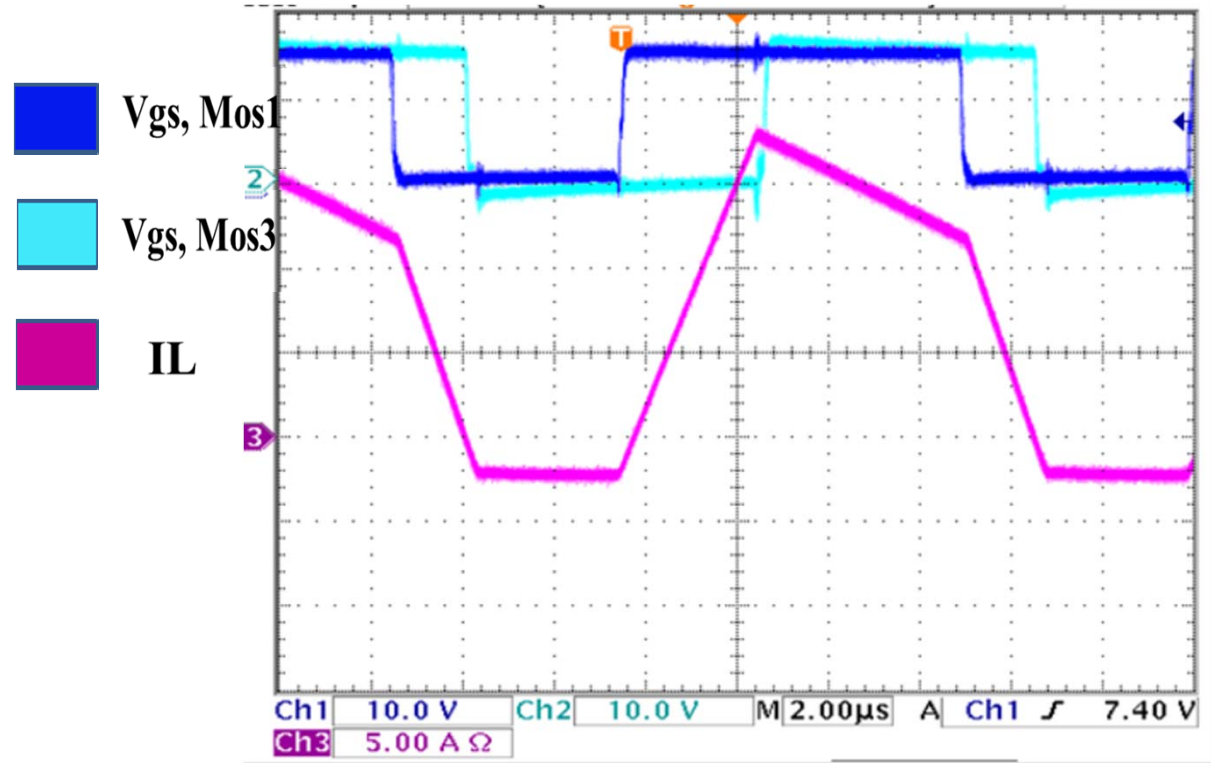
非隔離型雙向半橋轉換器



電路架構模擬



單組5kW電路實體



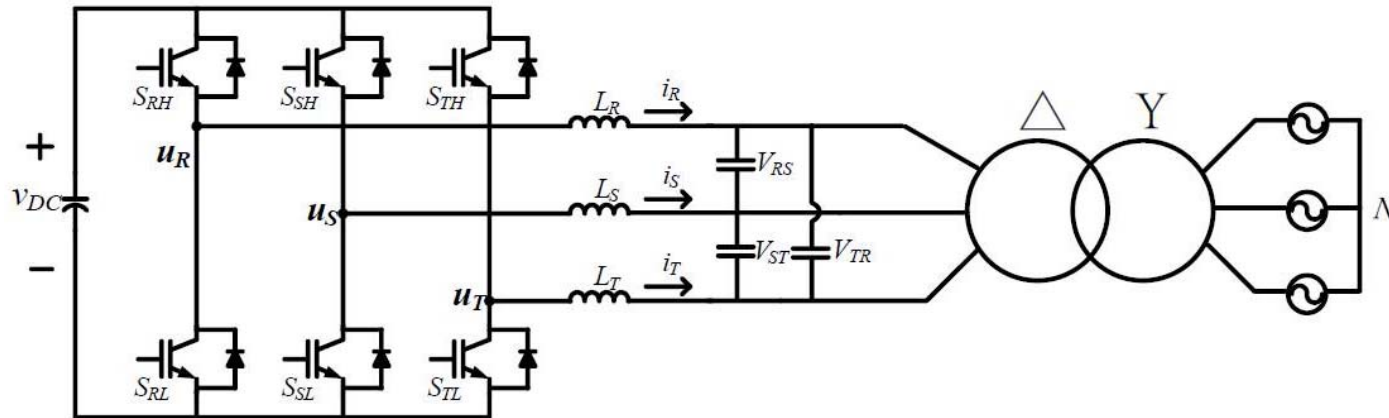
放電模式下，功率開關訊號與電感電流實測波形

放電模式下，最高效率可達**97%以上**、滿載效率**95.5%以上**、平均效率**96%以上**

Vin(V)	Iout(A)	Vout(V)	Pout(kW)	Eff(%)
380	0.997	450.8	0.449	83.18
	1.992	450.4	0.882	91.9
	3.015	449.9	1.356	96.99
	4	449.5	1.8	97.53
	5	449.1	2.209	96.98
	6.015	449	2.7	96.8
	7.1	449	3.18	96.4
	8.1	448.8	3.63	96.2
	9.4	448.8	4.2	96.1
	10.2	448.7	4.57	95.8
	11.3	448.6	5.062	95.5

## (五) 大功率轉換器模擬

### ➤ 雙向三相全橋換流器



電路架構

電氣規格

$$\begin{bmatrix} u_{RS} \\ u_{ST} \end{bmatrix} = \begin{bmatrix} L_R & -L_S \\ L_T & L_S + L_T \end{bmatrix} \begin{bmatrix} \frac{di_R}{dt} \\ \frac{di_S}{dt} \end{bmatrix} + \begin{bmatrix} v_{RS} \\ v_{ST} \end{bmatrix}$$

動態狀態空間模型

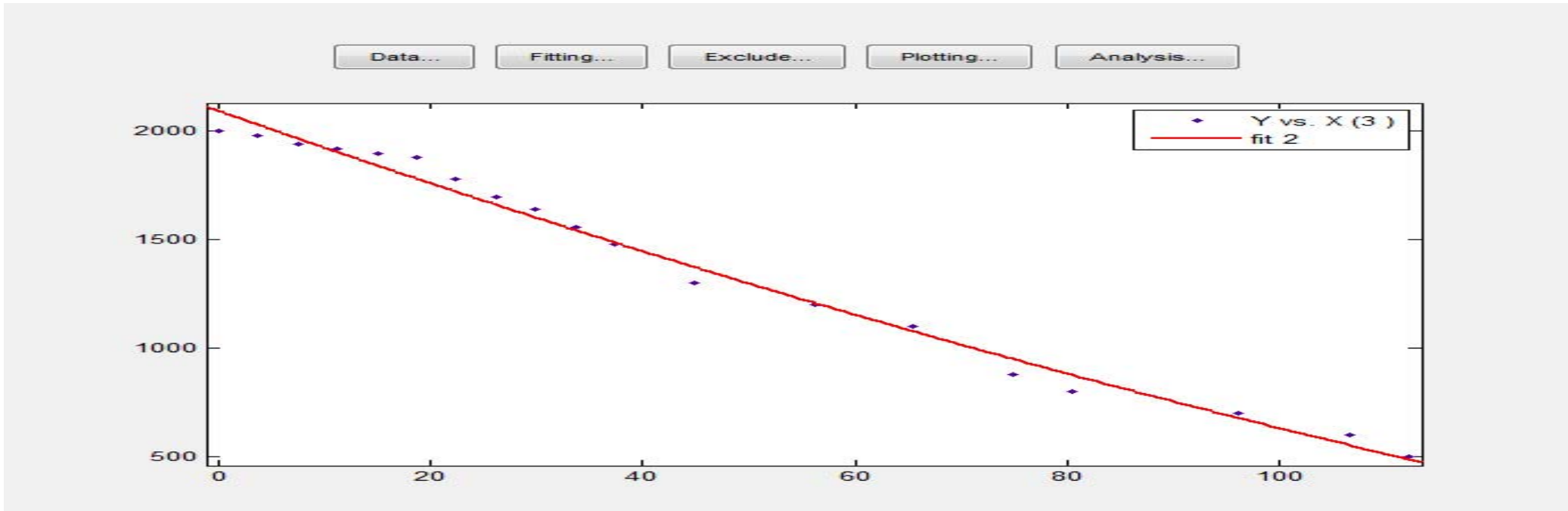
輸入電壓	700 V
輸出電壓	220 V <sub>rms</sub>
輸出功率	30 kVA
頻率	60 Hz

## 電感與電流變化建表

Current I(A)	電感(H)
3.738988	1977.03
7.477975	1937.09
11.21696	1917.12
14.95595	1897.15
18.69494	1877.18
22.43393	1777.33
26.17291	1697.45
29.9119	1637.54
33.65089	1557.66

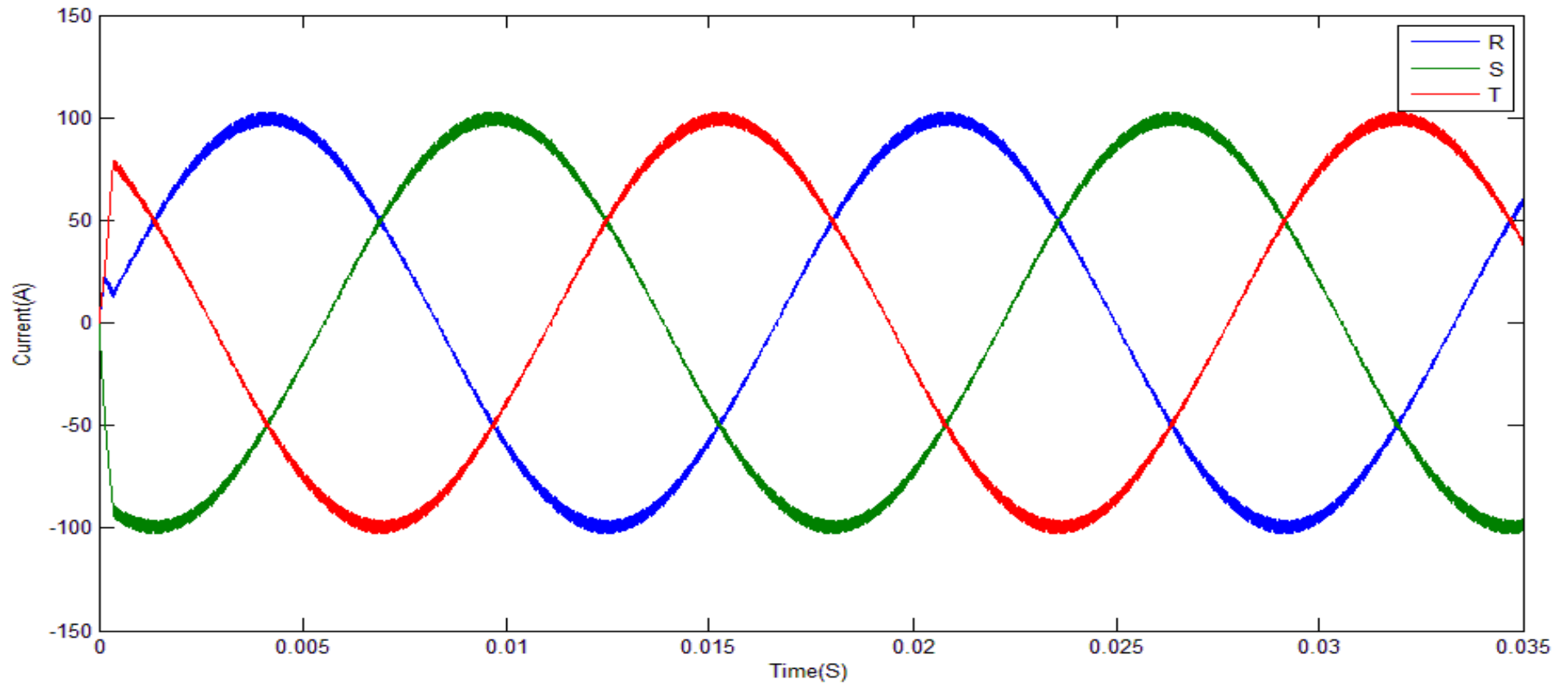
Current I(A)	電感(H)
37.38988	1477.78
44.86785	1298.05
56.08481	1198.2
65.43228	1098.35
74.77975	878.68
80.38823	798.8
96.09198	698.95
106.5611	599.1
112.1696	499.25

## 電感與電流使用CFTOOL尋找近似曲線



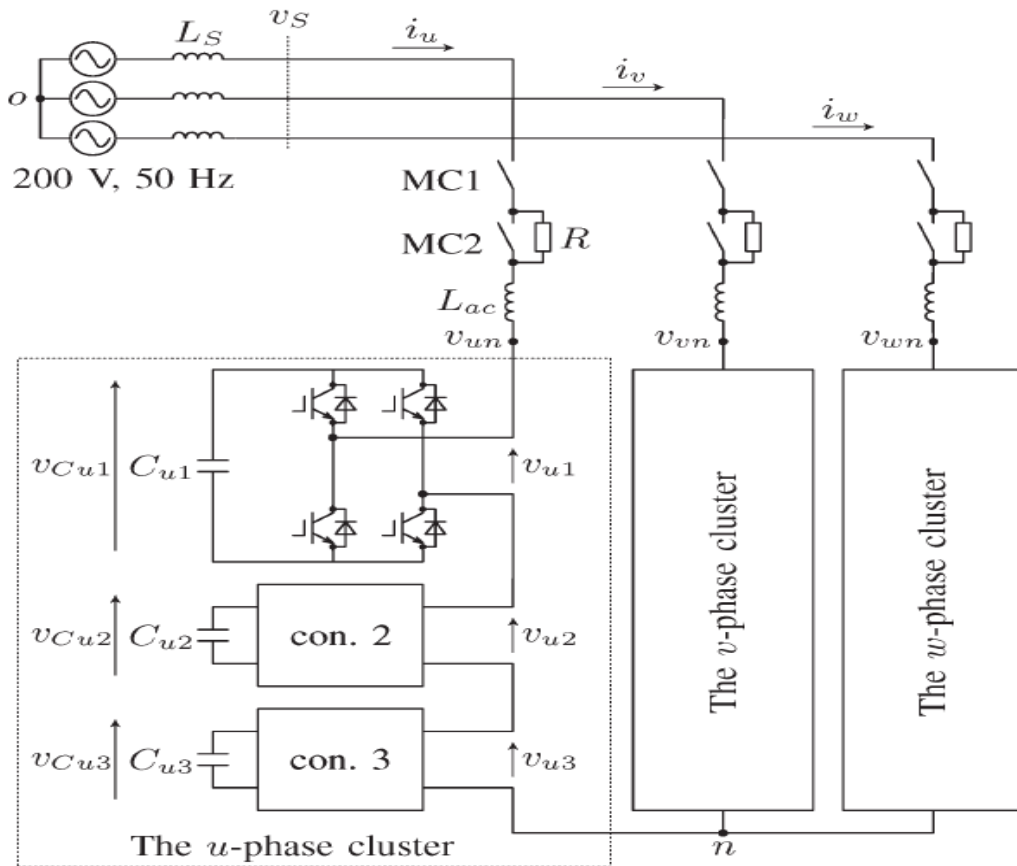
近似方程式： $L(I) = 0.02584I^2 - 17.2I + 2092$

## 市電併聯模式之三相電感電流波形





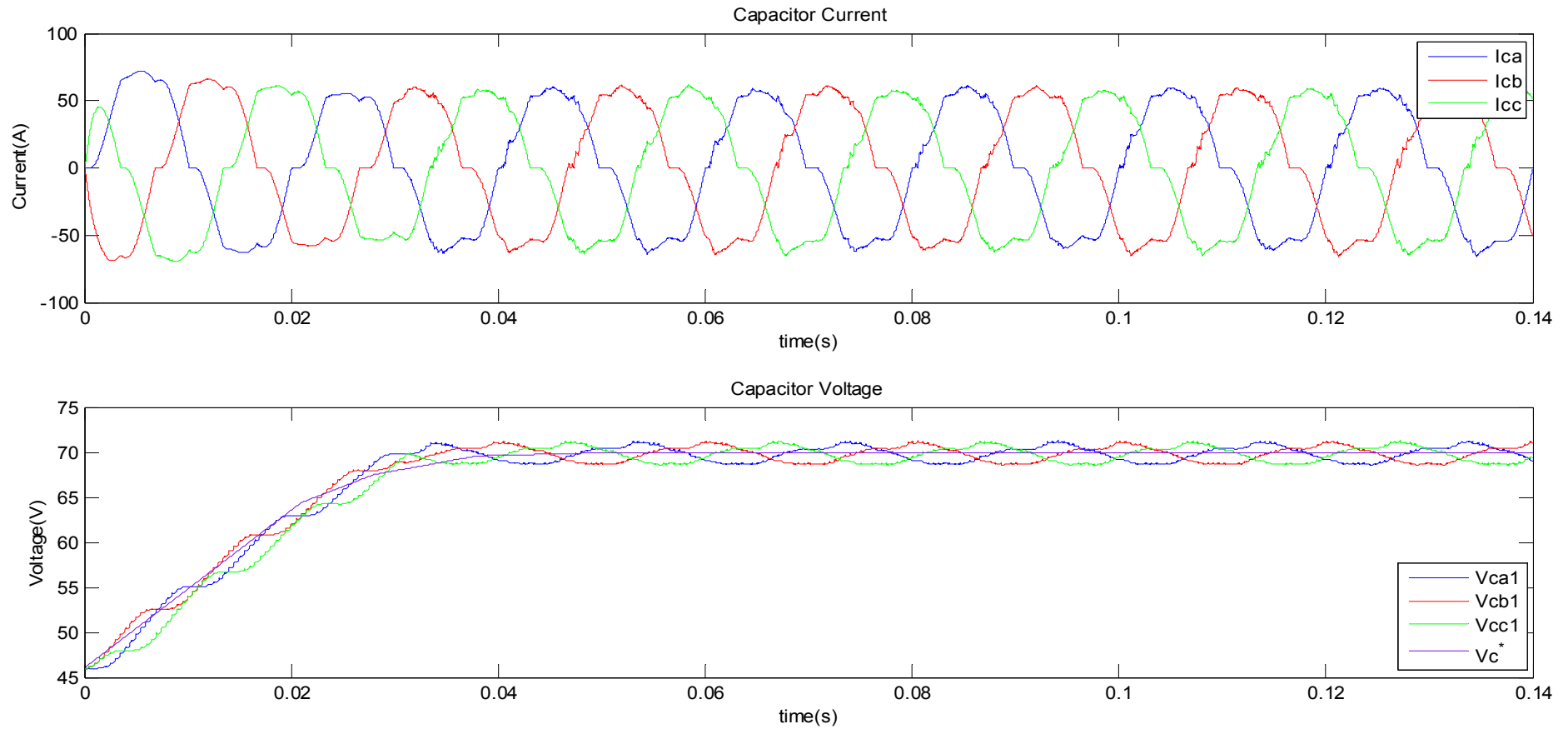
## ➤ 多階轉換器



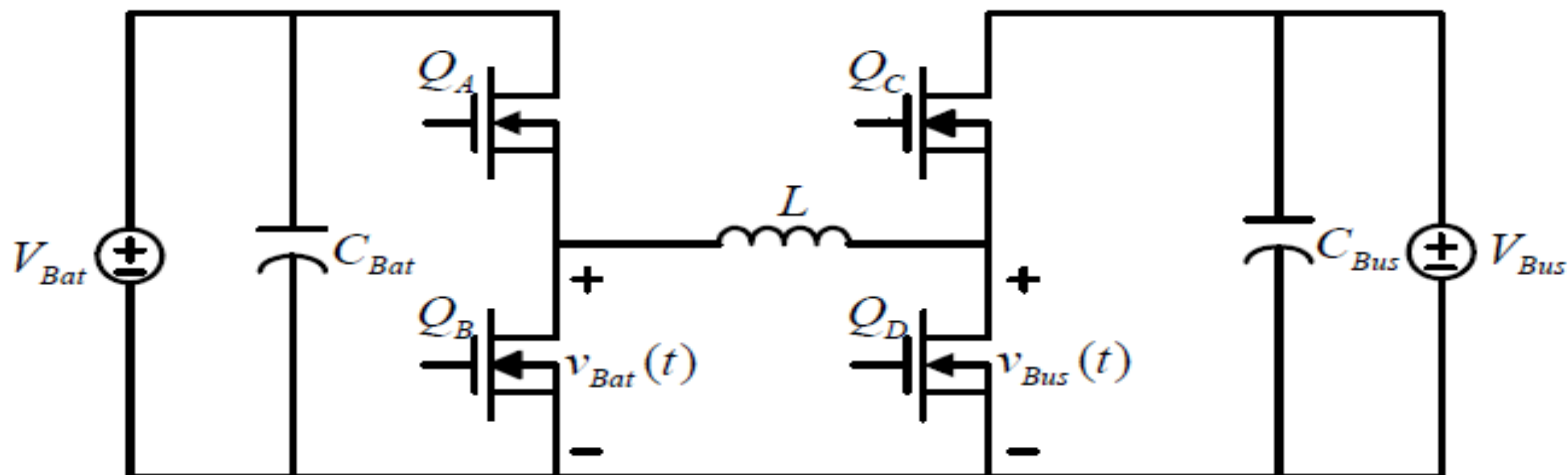
電路參數:

Rated reactive power	$Q$	10 kVA
Nominal line-to-line rms voltage	$V_S$	200 V
Background system inductance	$L_S$	48 $\mu$ H (0.4%)
AC inductor	$L_{ac}$	1.2 mH (10%)
Starting resistor	$R$	10 $\Omega$
DC capacitor voltage reference	$v_C^*$	60 ~ 70 V
DC capacitor capacitance	$C$	16,400 $\mu$ F
Unit capacitance constant	$H$	36 ms at 70 V
Carrier frequency for PWM		1 kHz
Low-pass-filter time constant	$T$	11 ms
Dead time in each converter		2 $\mu$ s

## 多階轉換器電容電壓追蹤波形

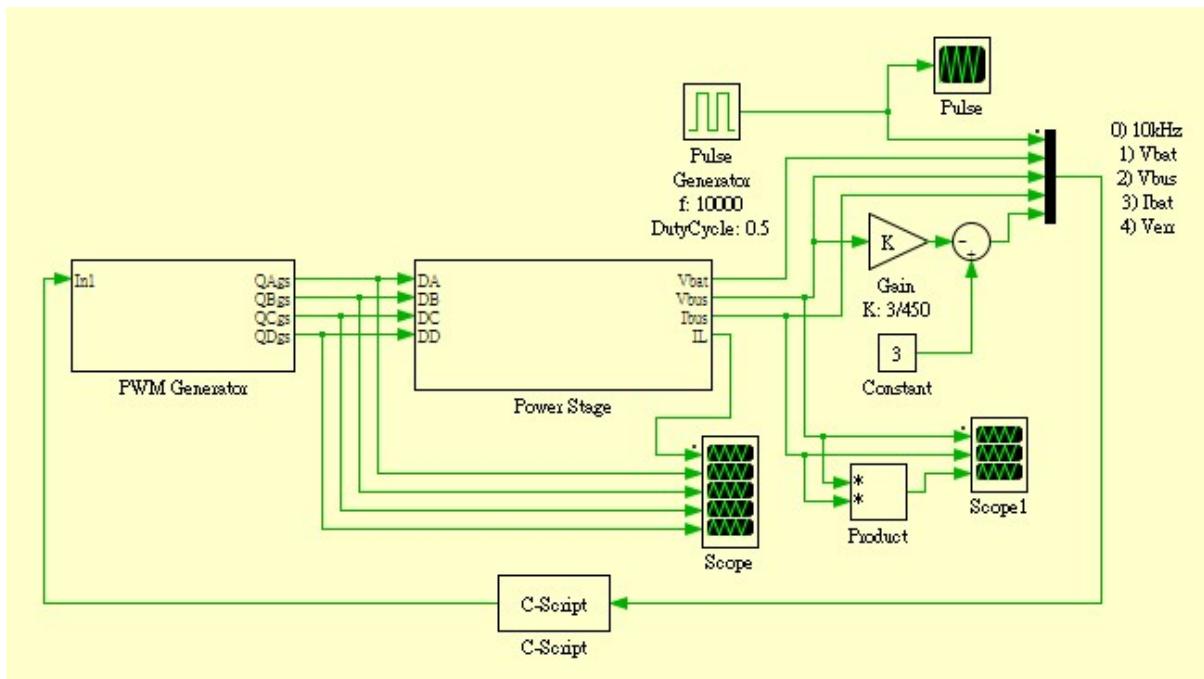


➤ 20kW充/放電器



電池電壓 $V_{Bat}$	350~400V
匯流排電壓 $V_{Bus}$	450V
輸出功率 $P_o$	5kW
開關工作頻率 $f_s$	80kHz

## 電力級加控制級模組(PLECS)



C-Script parameters: HalfBridge\_BoostMode\_Ver2/Control...

Setup Code

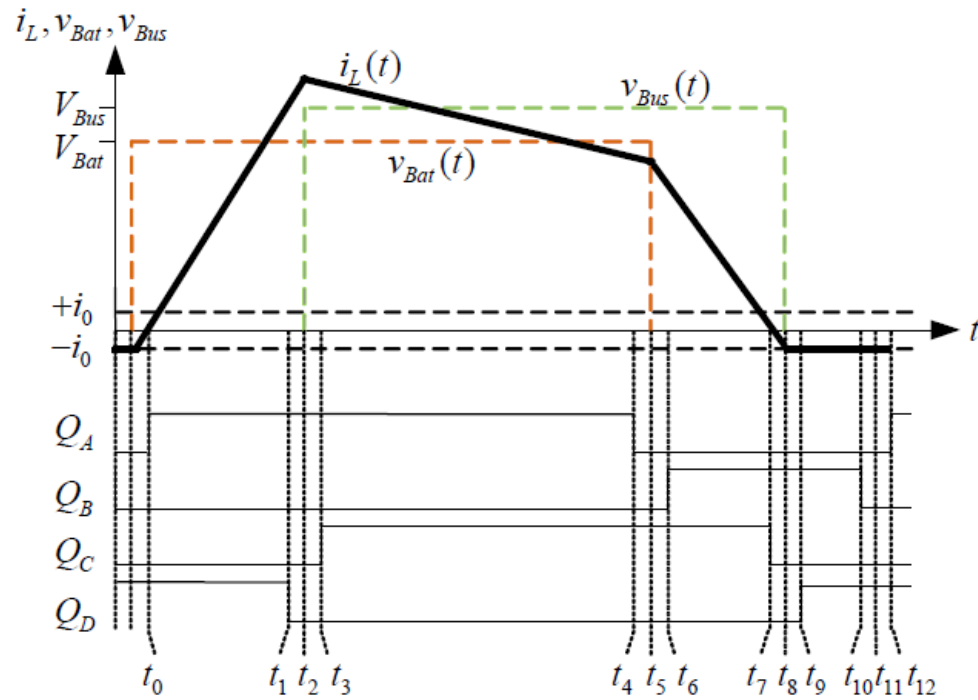
Code declarations

```

1 double max = 10, min = -10;
2 double Kp = 0.44, Ki = 0.044;
3 double Tp = 0.0000125, Lm = 0.000047, Io_min = 2.;
4 double str = 0;
5 double Vbus = 0, Vbat = 0, Verr = 0, Ibus = 0;
6 double x0 = 0, y0 = 0;
7 double t, Pout;
8 double temp0 = 0, temp1 = 0, temp2 = 0, temp3 = 0;
    
```

OK Cancel Apply Help

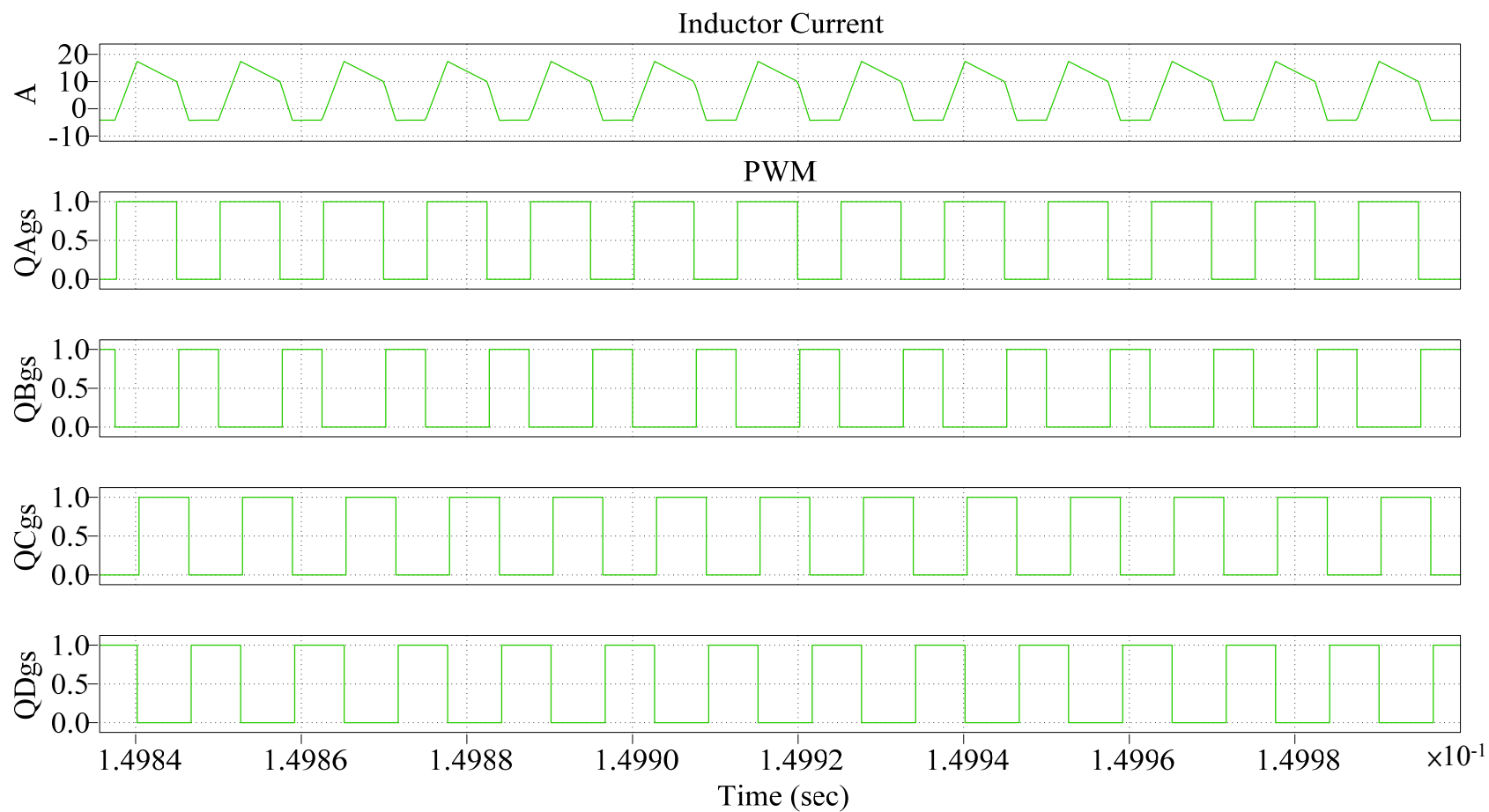
### 放電模式電感電流及開關波形



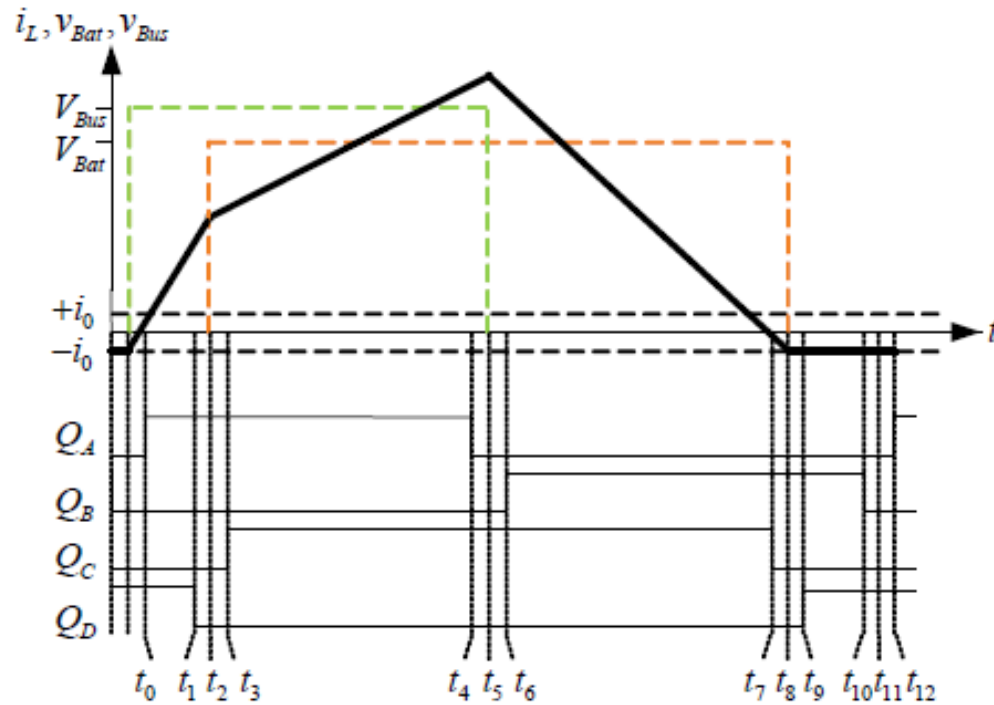
### 放電模式2.5kW模擬條件：

電池電壓 $V_{Bat}$	380 V
匯流排電壓 $V_{Bus}$	450 V
輸出功率 $P_o$	2.5 kW
開關工作頻率 $f_s$	80 kHz
電感 $L$	47 $\mu$ H

## 電感電流與PWM波形圖



## 充電模式電感電流及開關波形



## 充電模式2.5kW模擬條件：

電池電壓 $V_{Bat}$	380 V
匯流排電壓 $V_{Bus}$	450 V
輸出功率 $P_o$	2.5 kW
開關工作頻率 $f_s$	80 kHz
電感 $L$	47 $\mu$ H



## 電感電流與PWM波形圖

